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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/781,560	02/18/2004	Hung K. Cheung	P05802 (NATI15-05802)	9294
23990	7590	01/25/2006	EXAMINER	
DOCKET CLERK P.O. DRAWER 800889 DALLAS, TX 75380				COX, CASSANDRA F
			ART UNIT	PAPER NUMBER
			2816	

DATE MAILED: 01/25/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/781,560	CHEUNG ET AL.
	Examiner	Art Unit
	Cassandra Cox	2816

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).

Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 14 November 2005.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-23 is/are pending in the application.
4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) 7-20 is/are allowed.

6) Claim(s) 1-6, 21 and 22 is/are rejected.

7) Claim(s) 23 is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 15 July 2004 is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____

4) Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ .
5) Notice of Informal Patent Application (PTO-152)
6) Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-6 and 21-22 are rejected under 35 U.S.C. 102(b) as being anticipated by Shankar et al. (U.S. Patent No. 5,526,391).

In reference to claim 1, Shankar discloses in Figure 1 an edge counter comprising: an input receiving an input signal (CLOCK) and an output on which an output signal (OUT) is driven; and a set of logic gates (24; see Figure 2) between the input and output, the logic gates configured to change a state of the edge counter with each transition of the input signal (CLOCK) and to produce an output signal having a cycle corresponding to a predetermined number (which is seen to be the final count value) of transitions of the input signal (see ABSTRACT). The same applies to claim 21 wherein the output signal (OUT) has a 50/50 duty cycle even when the predetermined number is odd (see ABSTRACT).

In reference to claim 2 Shankar discloses in the ABSTRACT that the predetermined number (final value) may be even or odd.

In reference to claim 3 Shankar discloses in Figure 2 wherein a signal path between the input and output through the logic gates includes a sequence of only two logic gates (64, 67). The same applies to claim 22.

In reference to claim 4, Shankar discloses in Figure 1 that the logic gates generate a set of intermediate signals (RST_{EVEN} , RST_{ODD}), at least one of the intermediate signals changing state in response to transition of the input signal.

In reference to claim 5, Shankar discloses that the edge counter is part of a clock divider. Furthermore wireless receivers comprising clock dividers are well-known in the art of which fact official notice is taken. The same applies to claim 6, wherein wireless communication systems including receivers and transmitters are well-known.

Allowable Subject Matter

3. Claims 7-20 are allowed.
4. Claim 23 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
5. The following is a statement of reasons for the indication of allowable subject matter: Claim 23 would be allowable because the closest prior art of record fails to disclose a circuit as shown in Figure 2 wherein the sequence of only two logic gates comprises one AND gate and one OR gate in combination with the rest of the limitations of the base claims and any intervening claims.
6. The following is an examiner's statement of reasons for allowance: Claims 7-20 are allowed because the closest prior art of record fails to disclose a method comprising the step of defining a number of intermediate signals sufficient to count a predetermined number of edges and deriving a set of logic gates based on the determined states in

combination with the rest of the limitations of the base claims and any intervening claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cassandra Cox whose telephone number is 571-272-1741. The examiner can normally be reached on Monday-Thursday from 7:00 AM to 4:30 PM and on.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

CC

01/17/06



TIMOTHY P. CALLAHAN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800